

J1033 U.S. PTO  
09/96672  
09/27/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

**U.S. UTILITY Patent Application**

<p>O.I.P.E.</p> <p>SCANNED <i>HP</i> <i>HP</i> 3 Q.A. <i>HP</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/966721	CONT/PRIOR F	CLASS 438	SUBCLASS 106	ART UNIT 2812	EXAMINER Zach
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## APPLICANTS

Takashi Hosaka

### 3. Results

Structure of a semiconductor module and method of manufacturing the same

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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